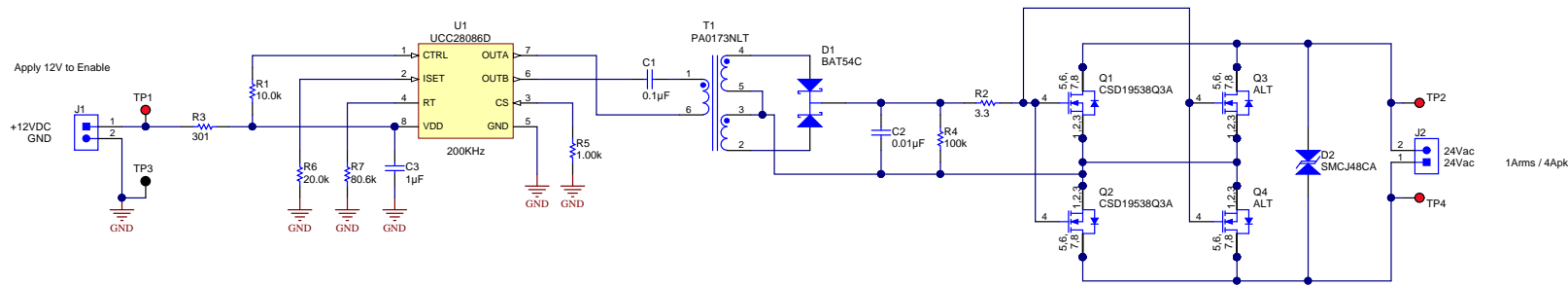


NOTES:



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Designed for: Public Release		Mod. Date: 1/6/2016	
Project Title: Solid State Relay		Sheet Title:	
Number: PMP12072	Rev: A	Assembly Variant: 001	Sheet: 1 of 1
SVN Rev: Version control disabled		File: PMP12072 REVA.SchDoc	Size: B
Drawn By:		Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	
Engineer: J Betten			



© Texas Instruments 2016